Chip back potential is the level which bulk silicon is maintained by on-chip connection, or it is the level to which the chip back must be connected when specifically stated below. If no potential is given the chip back should be isolated.

**.040”**

**.040”**

**ANODE**

**.025 x .025”**

**Top Material: Al**

**Backside Material: Ti/Ni/Ag**

**Bond Pad Size: .025” X .025”**

**Backside Potential: CATHODE**

**Mask Ref:**

**APPROVED BY: DK DIE SIZE .041” X .041” DATE: 11/10/21**

**MFG: SENSITRON THICKNESS .009” P/N: 1N5615**

**DG 10.1.2**

#### Rev B, 7/1